

# 3GPP 5G NR System Design & Verification Solution

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# Agenda

- Design Challenges for 5G Physical Layer Design Overview
- System Architecture Design
- Baseband RF Antenna Design
- Baseband RF Antenna Simulation Verification
- Early Emulation system for hardware test

# 5G Physical Layer Design Challenges

## Baseband Design Challenges

- Standard compliance test
- Massive MIMO algorithm verification

## RF Design Challenges

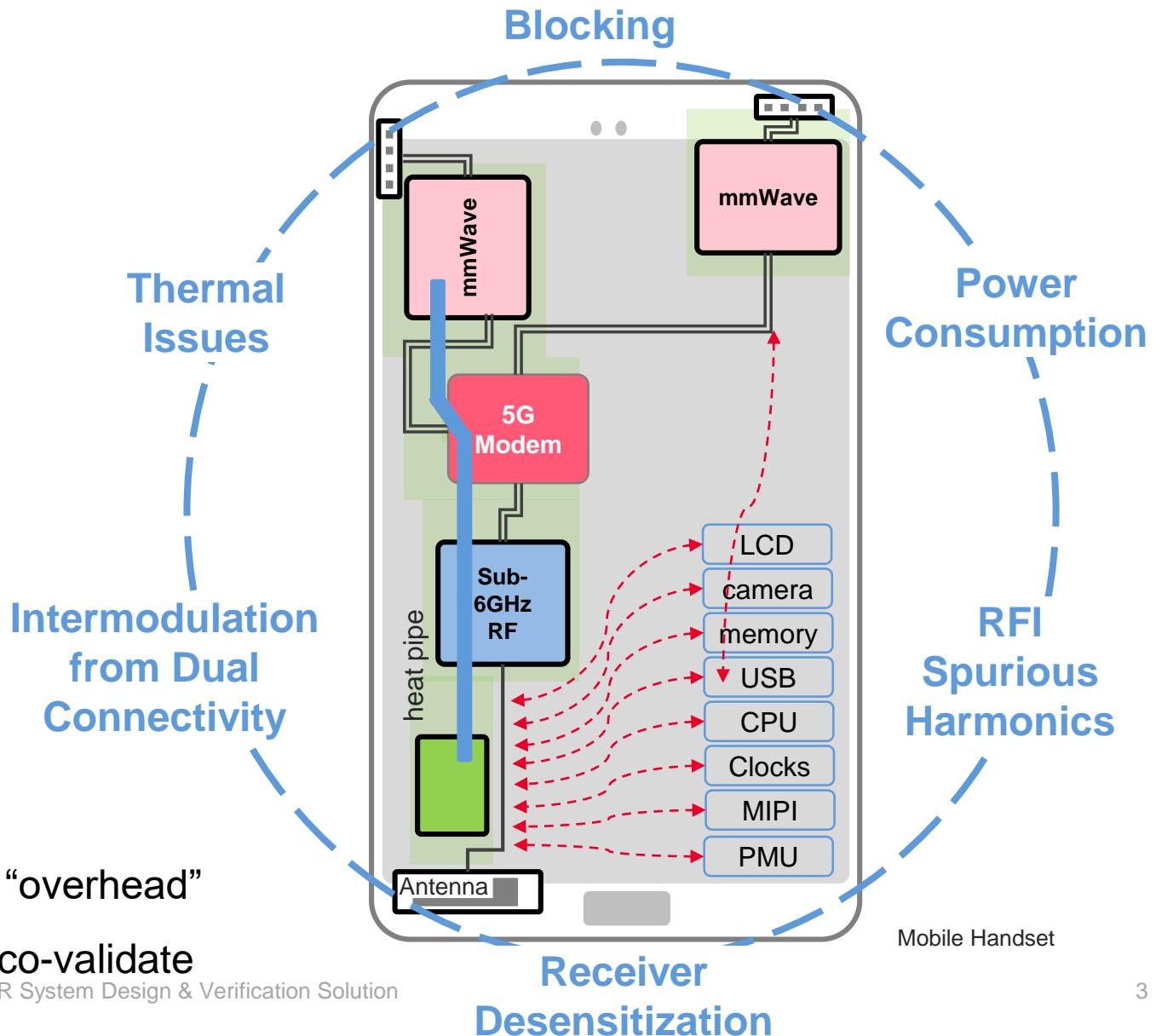
- Transceiver Design:
- Millimeter wave bands
  - Beamforming
  - Coupling Effect(multiple channel,RF/Antenna)
  - impedance mismatch

## Antenna Design Challenges

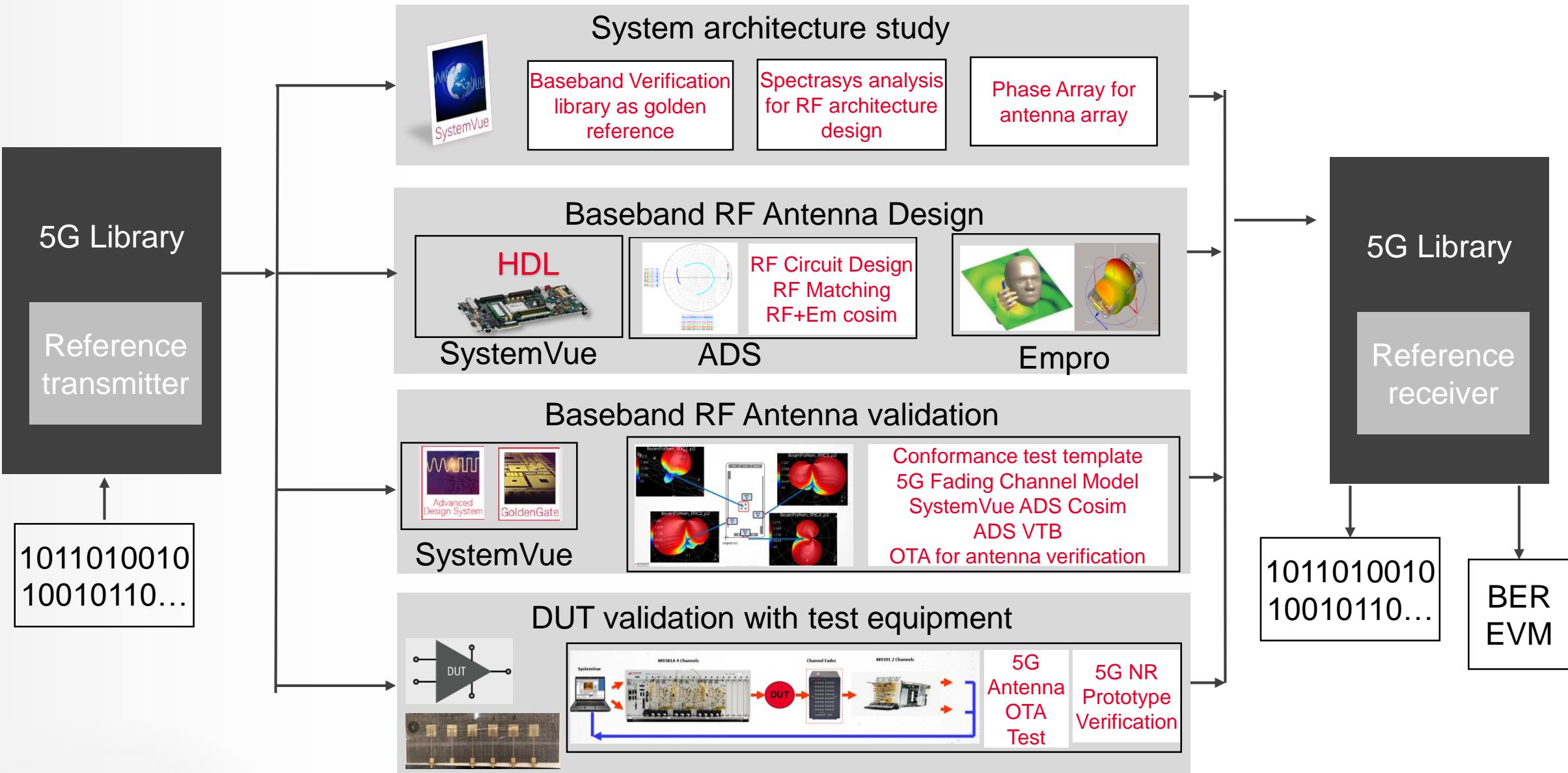
- mm wave array antenna design
- OTA performance verification

## System Integration Challenges

- Disjointed tool set
- One-directional, not predictive
- Low budget for “overhead”
- Difficult to co-validate



# Accelerate 5G Design with ADS SystemVue Empro



# What You Can Do for 5G?

USING SYSTEMVUE, ADS, GOLDENGATE & EMPRO

1. Standard Waveform Creation and Analysis
2. Architecting RF Transceivers
3. Phased Array and Beamforming
4. Link Level Performance
5. 5G OFDM Based Real time FPGA Prototype for Massive MIMO Verification
6. High Band RF Matching
7. MM wave Transmitter Design
8. Phased Array Antenna Design
9. Advanced End-To-End Link/OTA Performance validation
10. Early RD Hardware Verification

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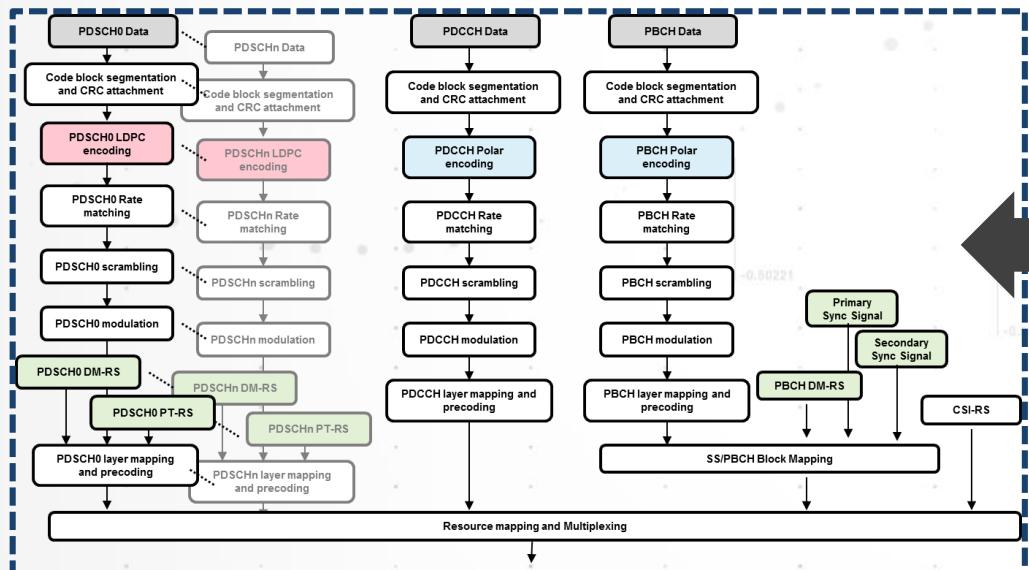
# 1. Standard Waveform Creation and Analysis

## Baseband: Trusted Algorithm Reference IP

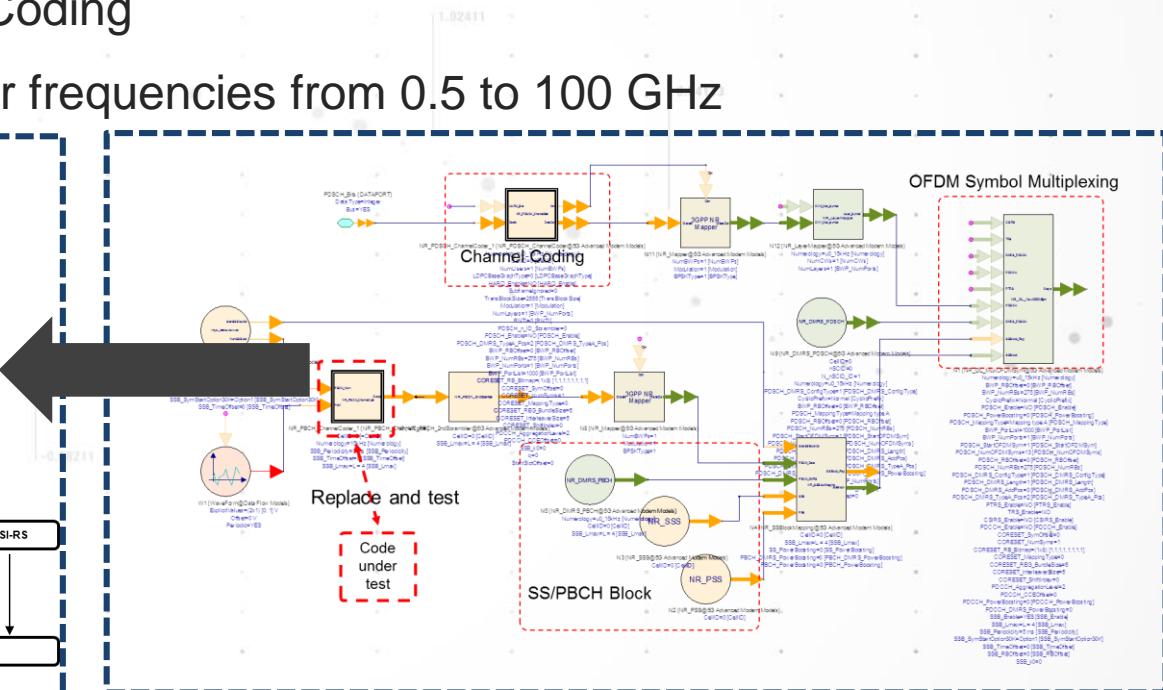


### Support Standards

- 3GPP TS 38.211 - Physical Channels and Modulation
- 3GPP TS 38.212 - Multiplexing and Channel Coding
- 3GPP TR 38.901 - Study on channel model for frequencies from 0.5 to 100 GHz



[ NR Baseband Digital Signal Processing ]



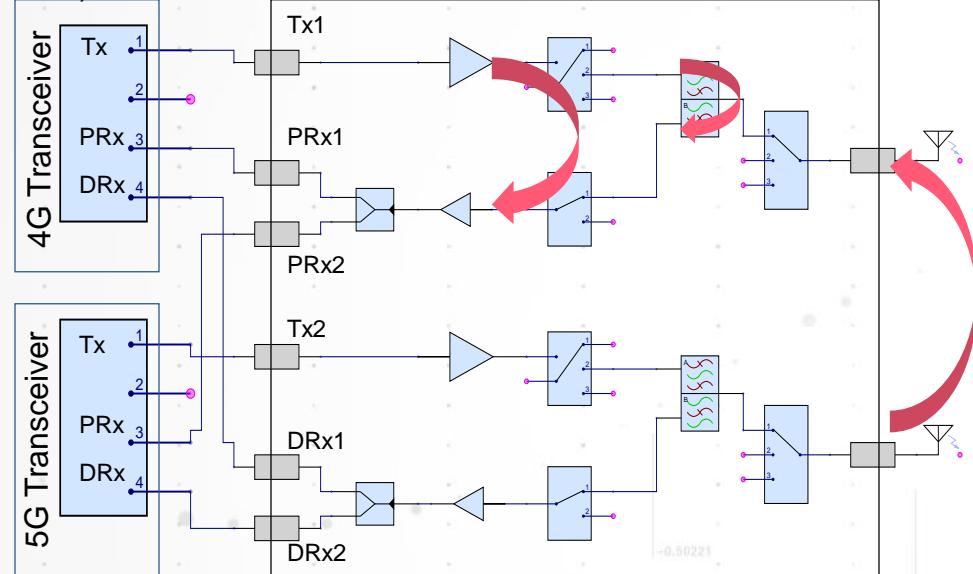
[ Model Based Design for NR Baseband DSP]

## 2. Architecturing RF Transceivers

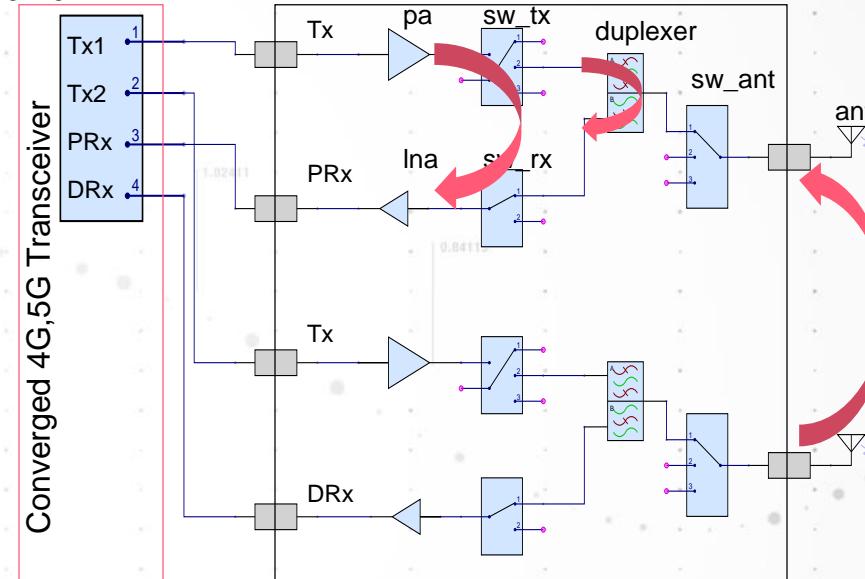


### FR1 Dual Connectivity UE Architectures

Note) for basic DC UE RF architectures information, refer to 3GPP TR 37.863-01-01



[ Separate 4G, 5G Transceivers, Dedicated UL PA ]



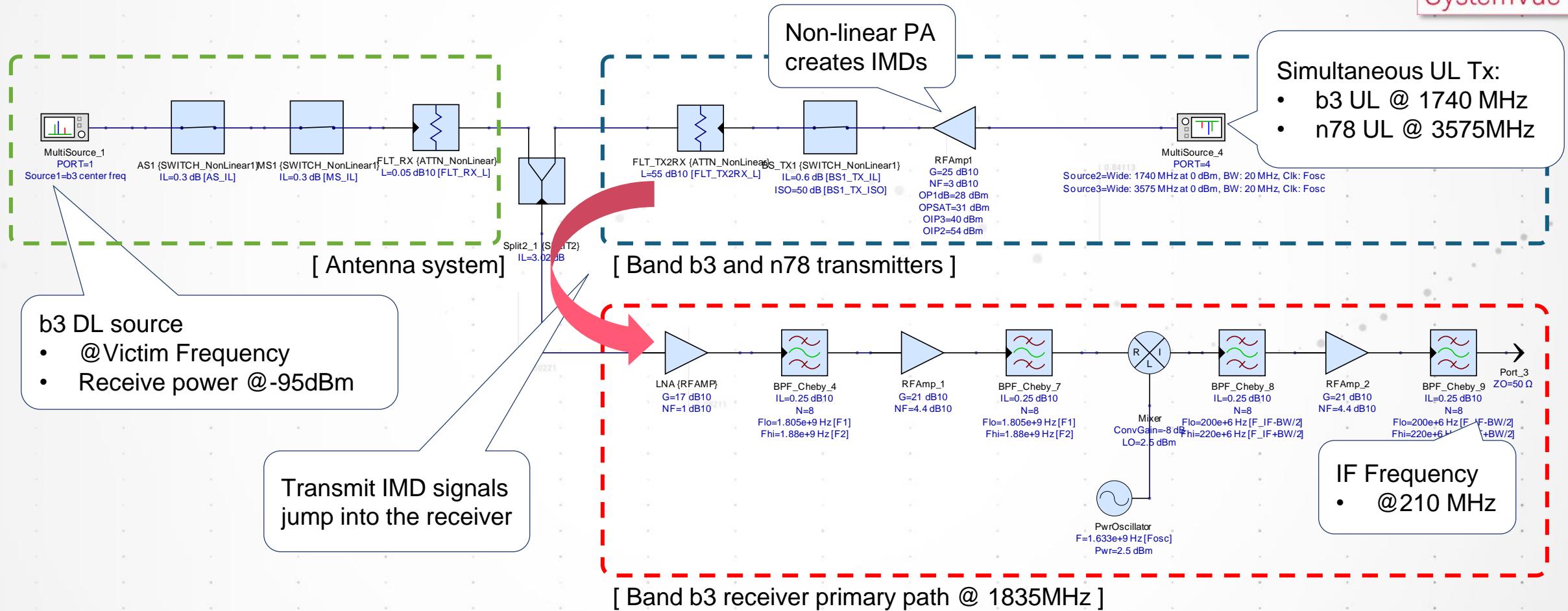
[ Converged 4G, 5G Transceivers, Dedicated UL PA ]

### Technical Issues:

- Complex 4G, 5G Transceivers and Multi-band RFFE(RF Front End) Design
- Dual Connectivity Simultaneous UL Produces IMD onto Active Receiver (Rx)
- Noise measurements in Rx band to estimate the impact of Tx excess noise

## 2. Multi-Radio Co-Existence

### FR1 Transceiver Modeling in Frequency Domain



## 2. Architecting RF Transceivers

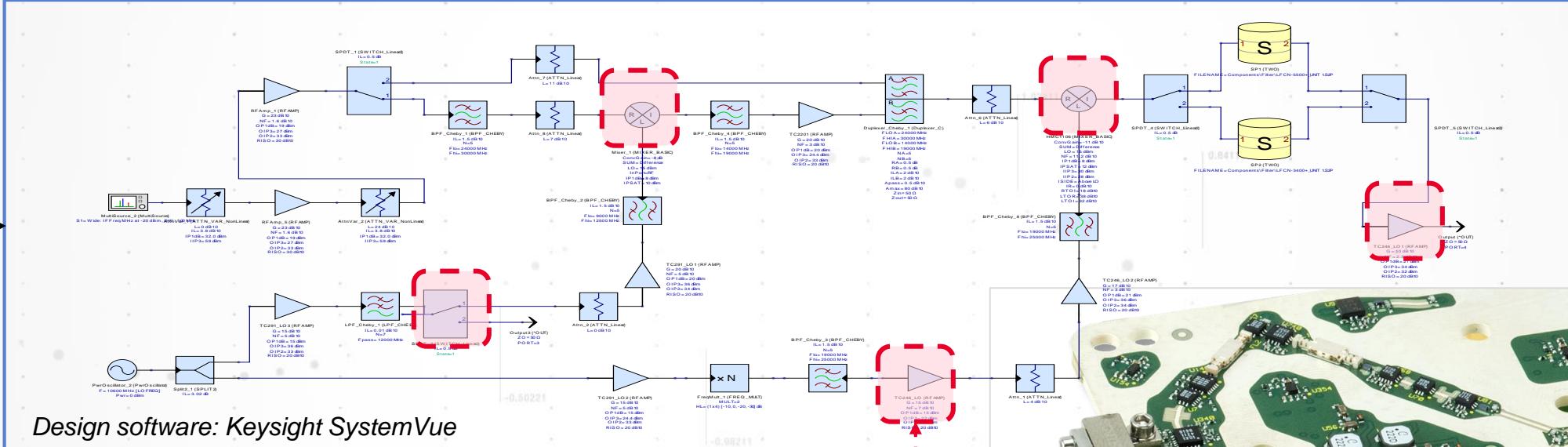
### mmWave Design Flow



SystemVue

System level modeling using circuit level MMIC design data

Iteration



complete MMIC design flow, Keysight ADS

#### Front End Schematic

- Load Pull – power and PAE
- PA – Initial design with linear and non-linear simulation
- Optimization Cockpit
- Robust Statistical Design
- **X-Parameters**

#### Back End Layout

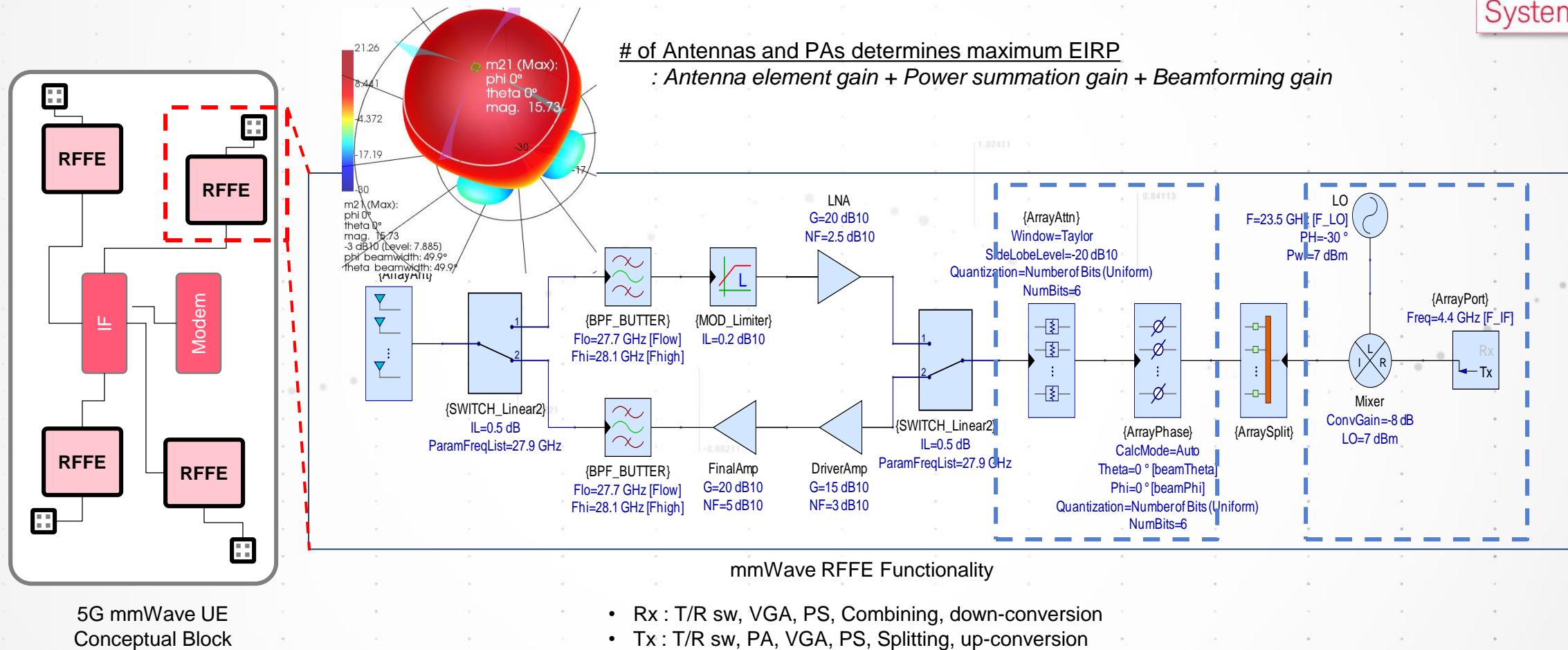
- Layout & 3D view Momentum
- DRC
- LVS
- Reticle Generation
- Package and Bond wire effects / 3D EM simulation



image: Keysight, 28GHz transceiver module

### 3. Phased Array and Beamforming

Antennas + Beamforming + Frequency Conversion



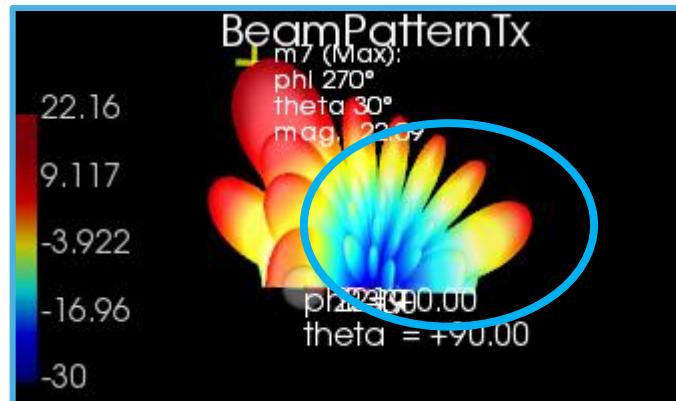
# Characterization of mmWave Components



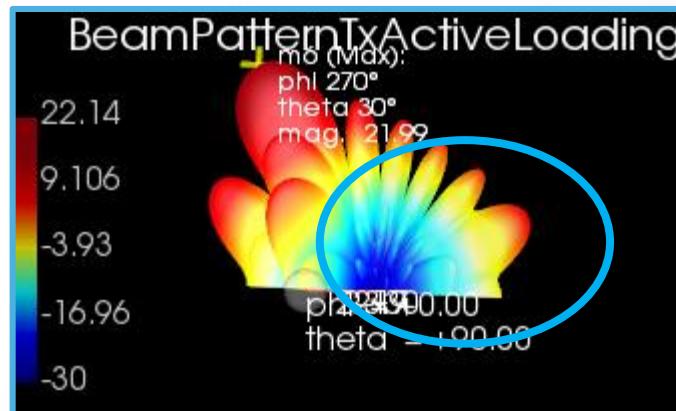
## Array Antenna

### Mutual Coupling

- Ideal coupling matrix: non-physical coupling based on the distance between the elements
- S-parameters: generated from electro magnetic simulation, real measurement. This is a physical basis and much more accurate
- Active impedance: various depends on array configuration, spacing between elements, and phase shift applied at each element
- Antenna element patterns



Ideal coupling matrix

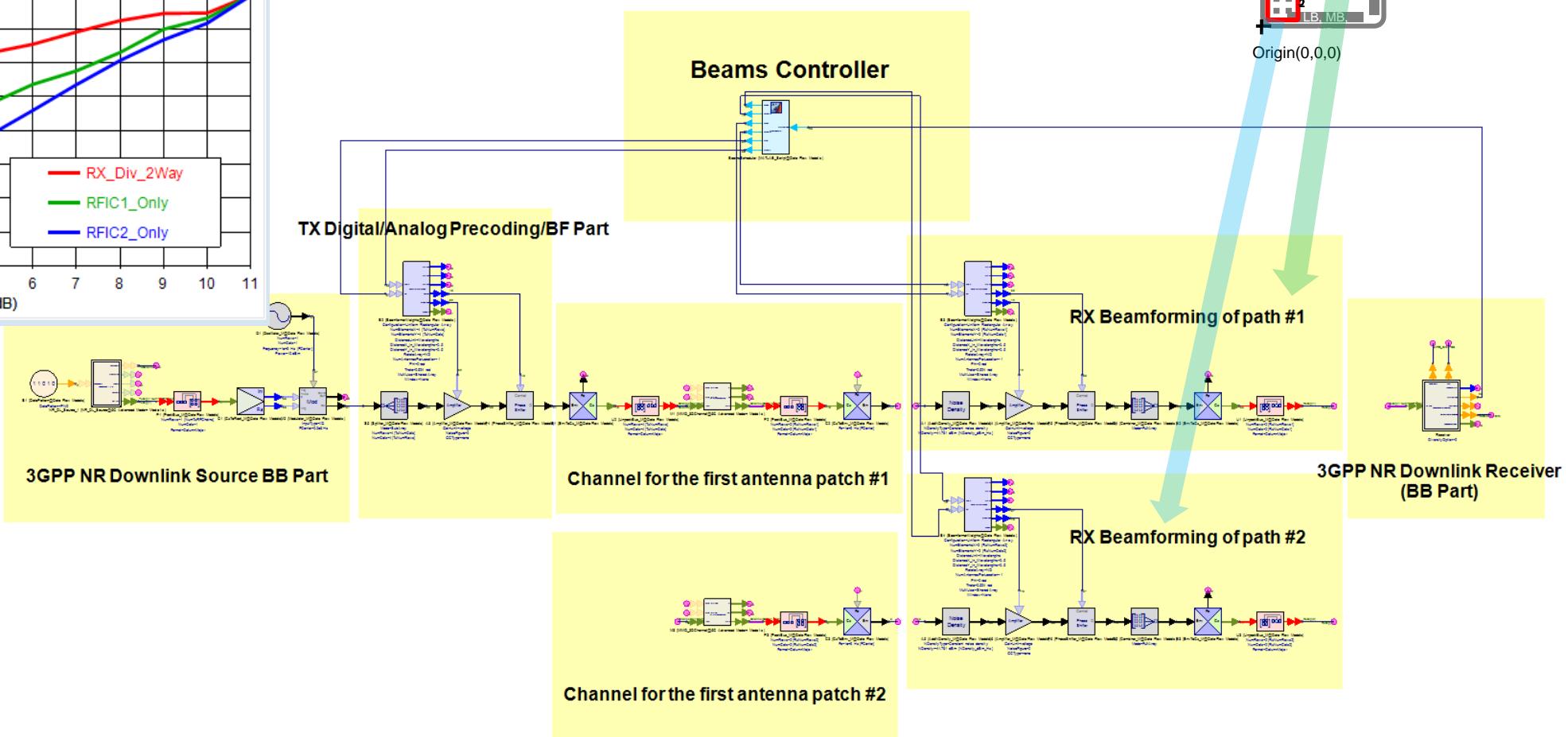
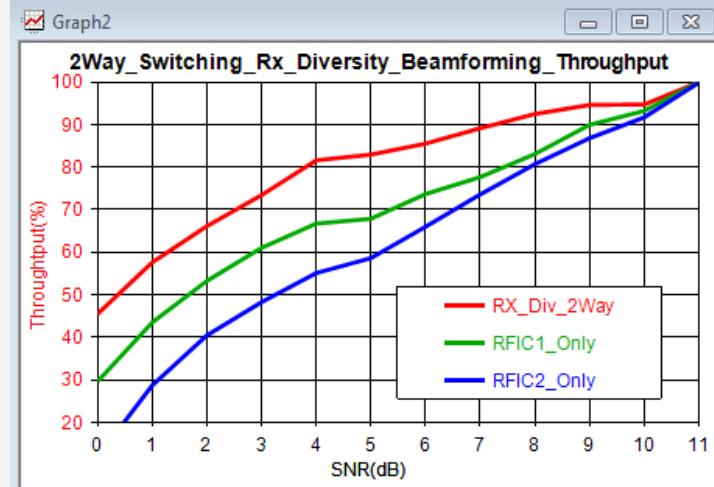


S-parameters

Increased null level from mutual coupling effect

# 4. Link Level Performance

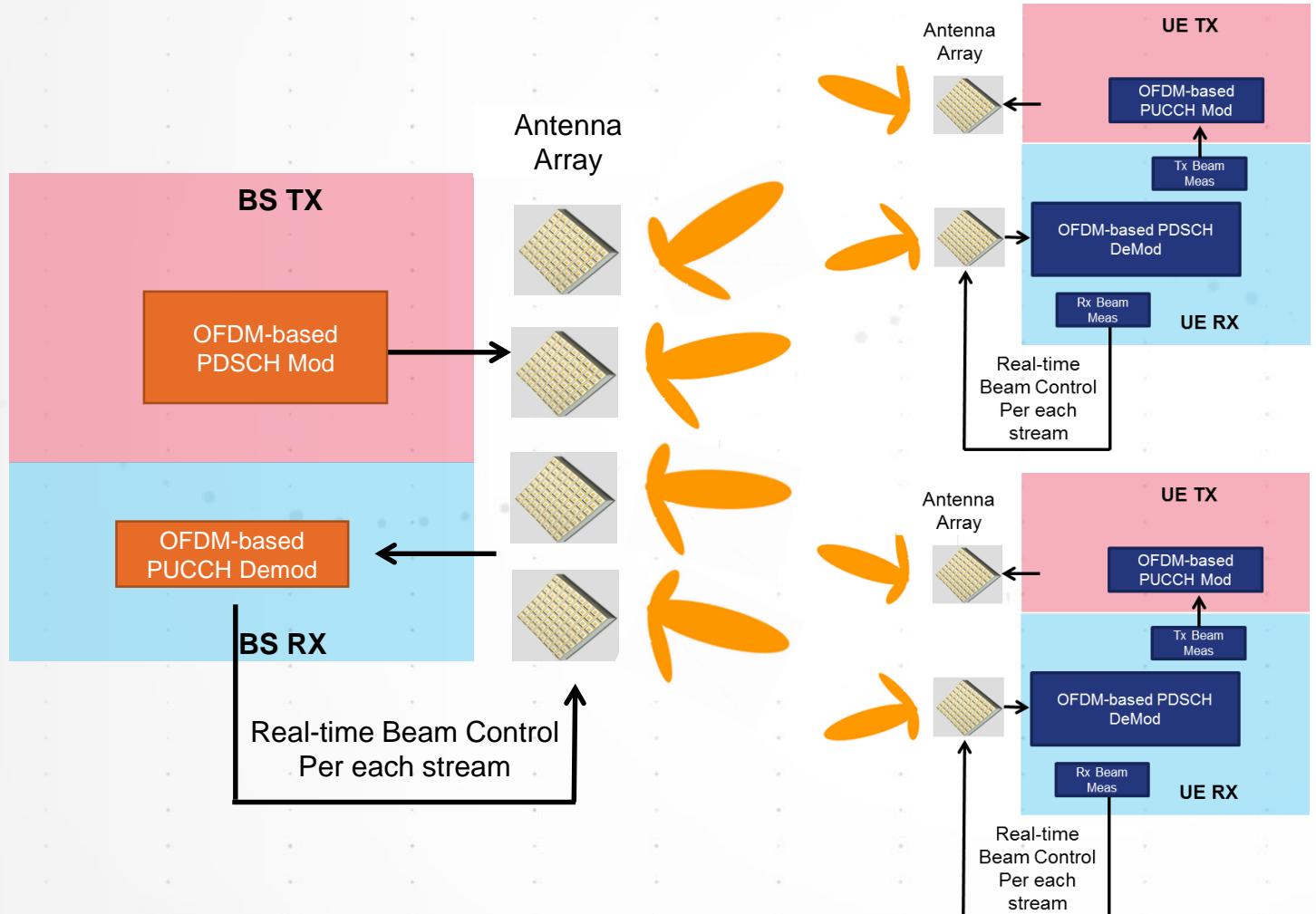
## A Glance of Future Diversity Case Including Beams



# Agenda

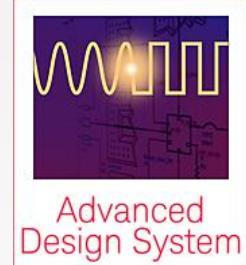
- Design Challenges for 5G Physical Layer Design Overview
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- Early emulation system for hardware test

# 5. 5G OFDM Based Real time FPGA Prototype for Massive MIMO Verification



- Provide the HLS IP for real time Massive MIMO FPGA Prototype
- Support real time Massive MIMO adaptive beamforming
- Support real time beam tracking  
Support TDD mode
- Support up to 4 streams in BS(2 UEs, each UE has 2 streams)
- Support throughput test

# 6. High Band RF Matching

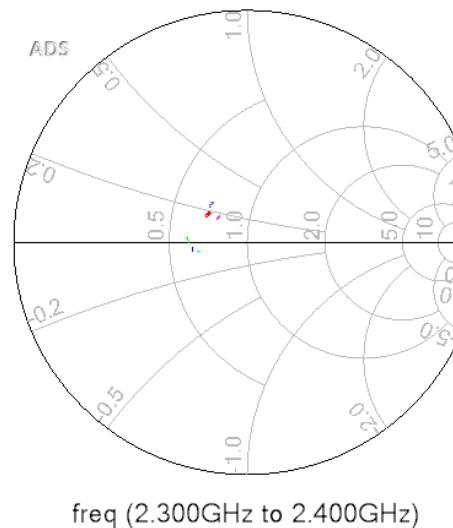


*Layout Import*



*EM-Schematic Co-sim*

cell\_3\_SI\_Analysis\_4..S(2,2)  
cell\_3\_SI\_Analysis\_4..S(1,1)  
Measured..S(2,2)  
Measured..S(1,1)  
S(2,2)  
S(1,1)



*Accurate Match with Meas.*

## Project output :

1) Reduce simulation time for RF team :

Previous : 1day /band → Keysight ADS Flow : 1hour /band

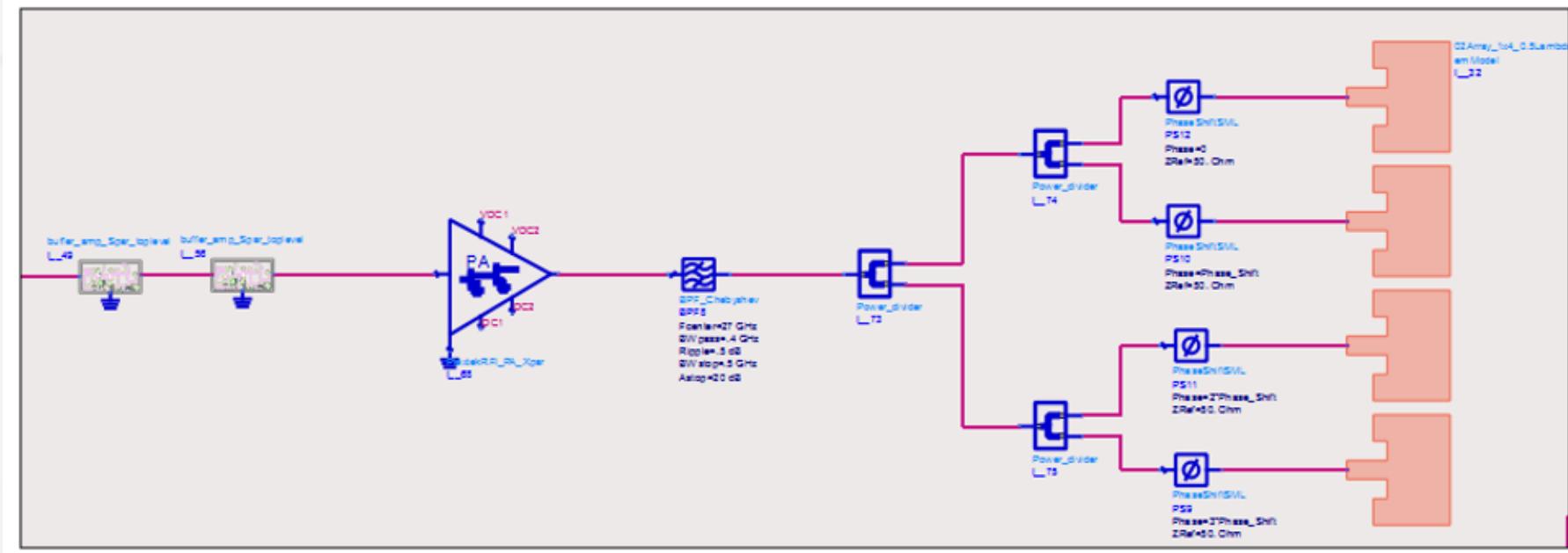
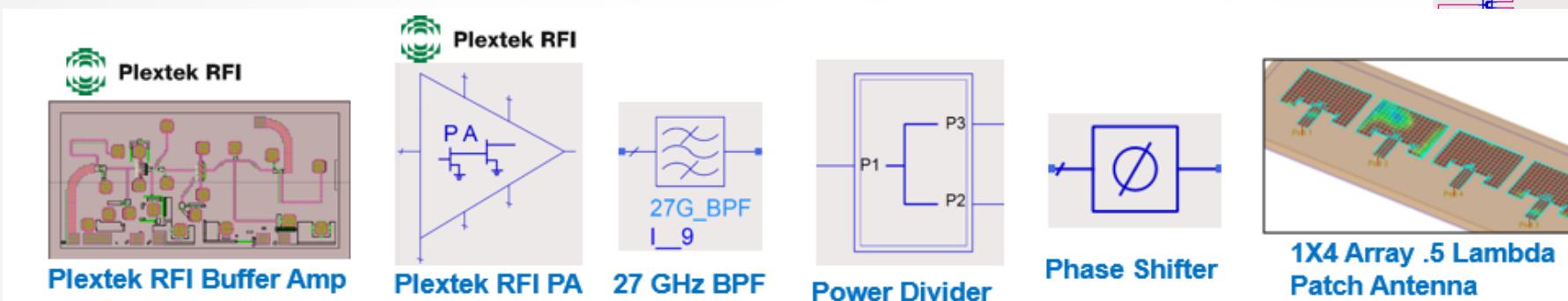
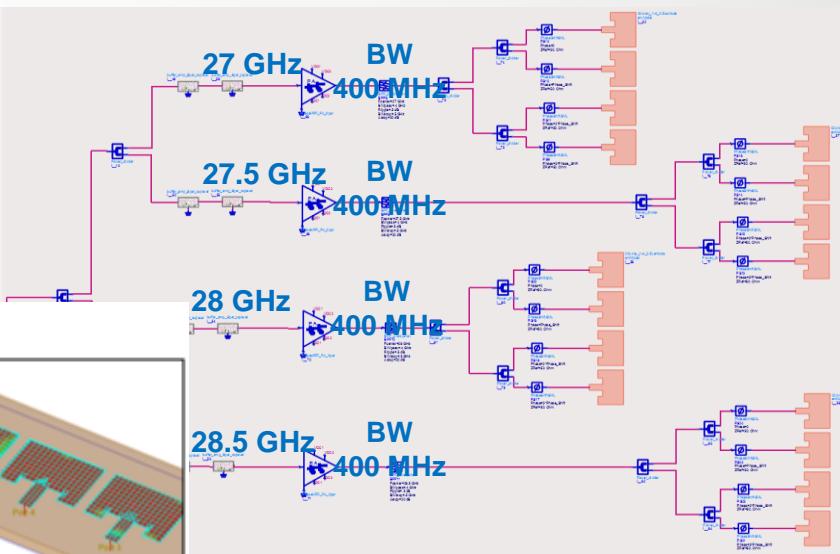
2~3weeks /board → Keysight ADS Flow: <3 Day/board

2) Great improved accuracy for high-band matching

Previous : Can't match meas. → Keysight Flow : Accurate match with meas.

# 7. MM wave Transmitter Design

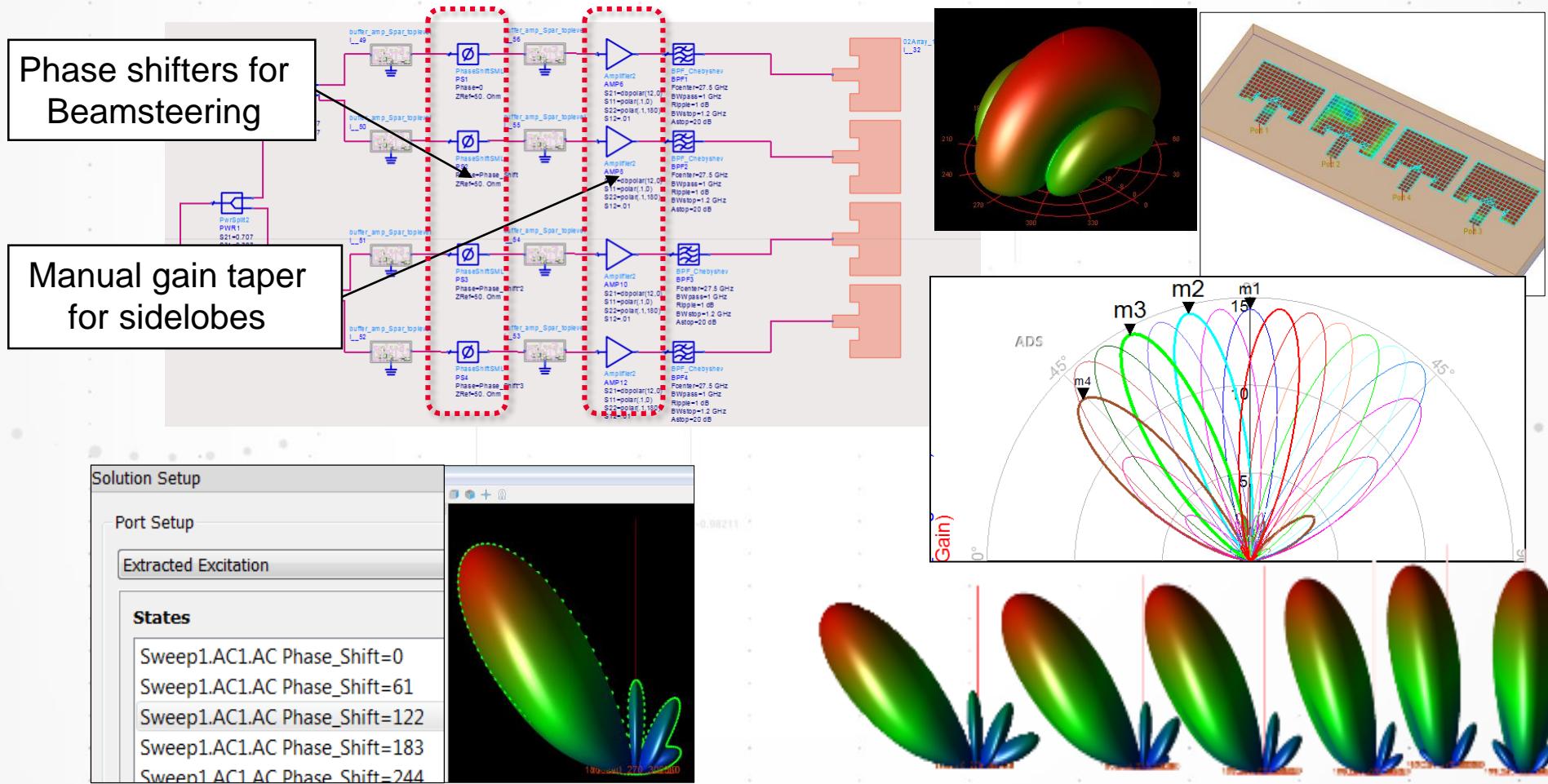
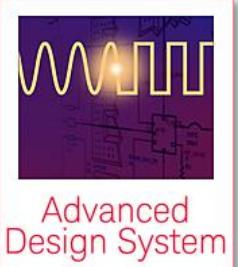
single pass sub-network



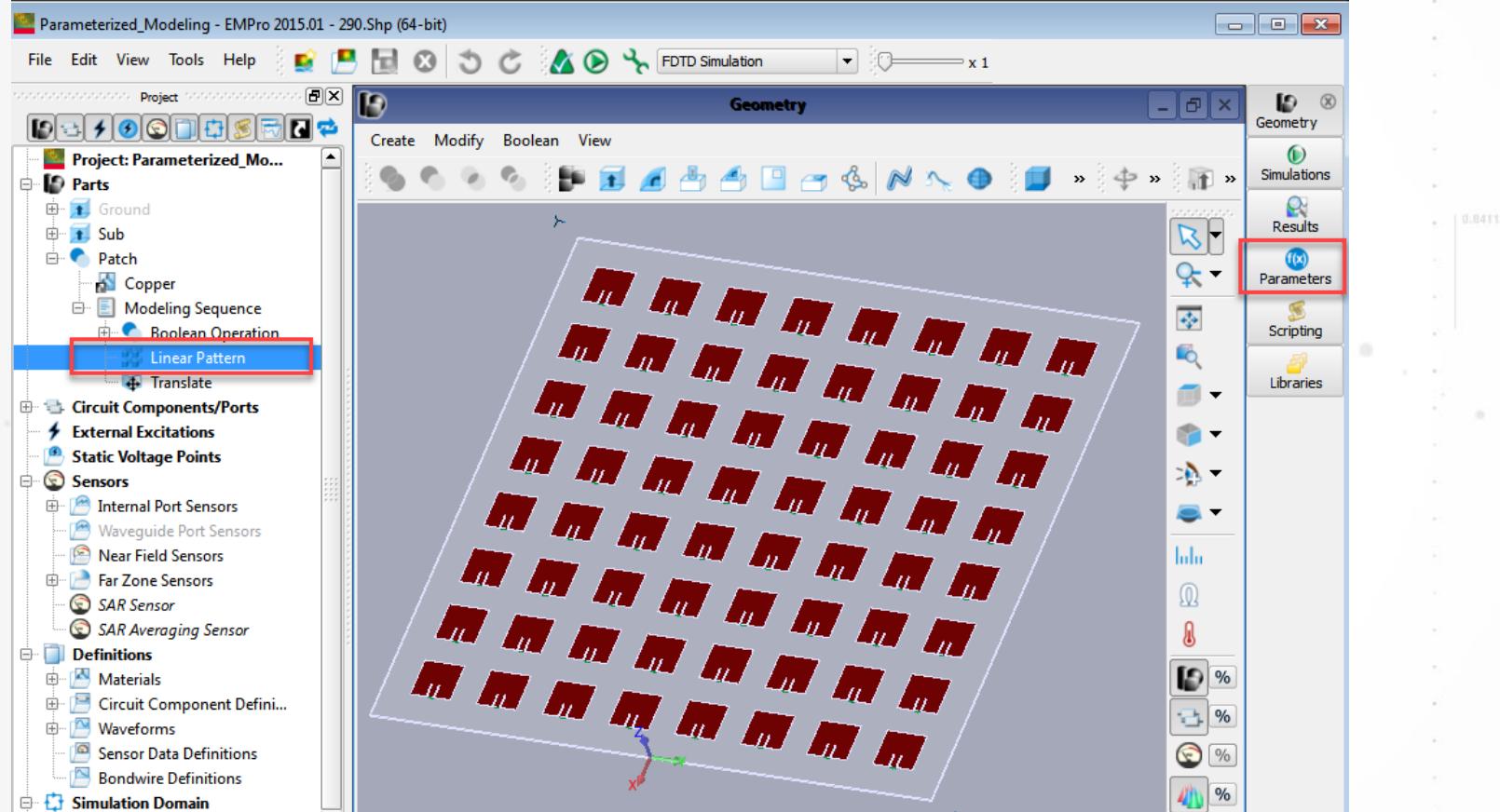
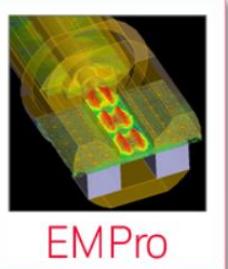
Advanced  
Design System

# 7. EM Co-Sim: 1x4 Transmitter Array in ADS/Momentum

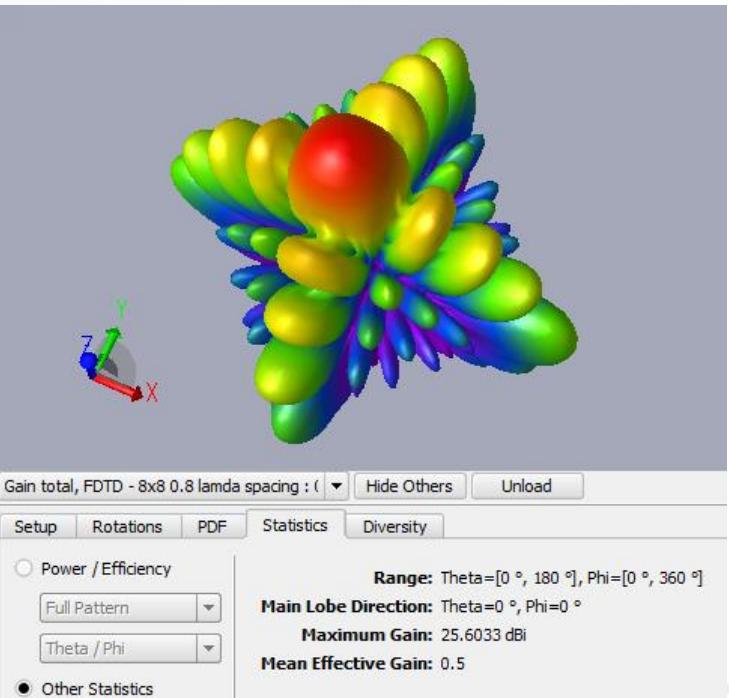
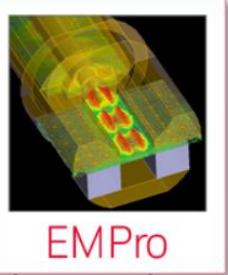
System / Circuit / EM Co-simulation and beam steering



# 8. Phased Array Antenna Design -- EMPro

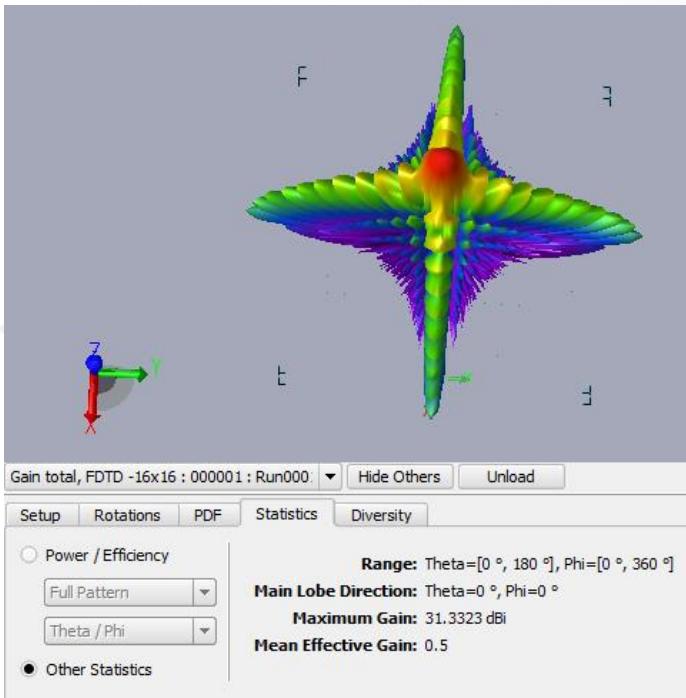


## 8. EMPro – 3D EM Simulation(FDTD)



8\*8 (0.8 lamda spacing)

4 K80 GPU  
4min, 40s

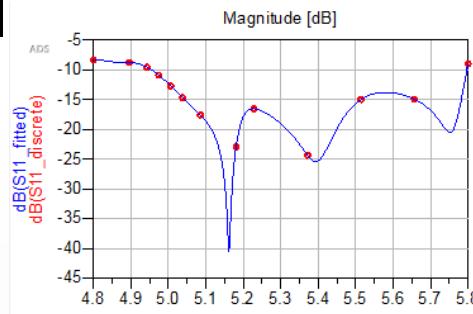
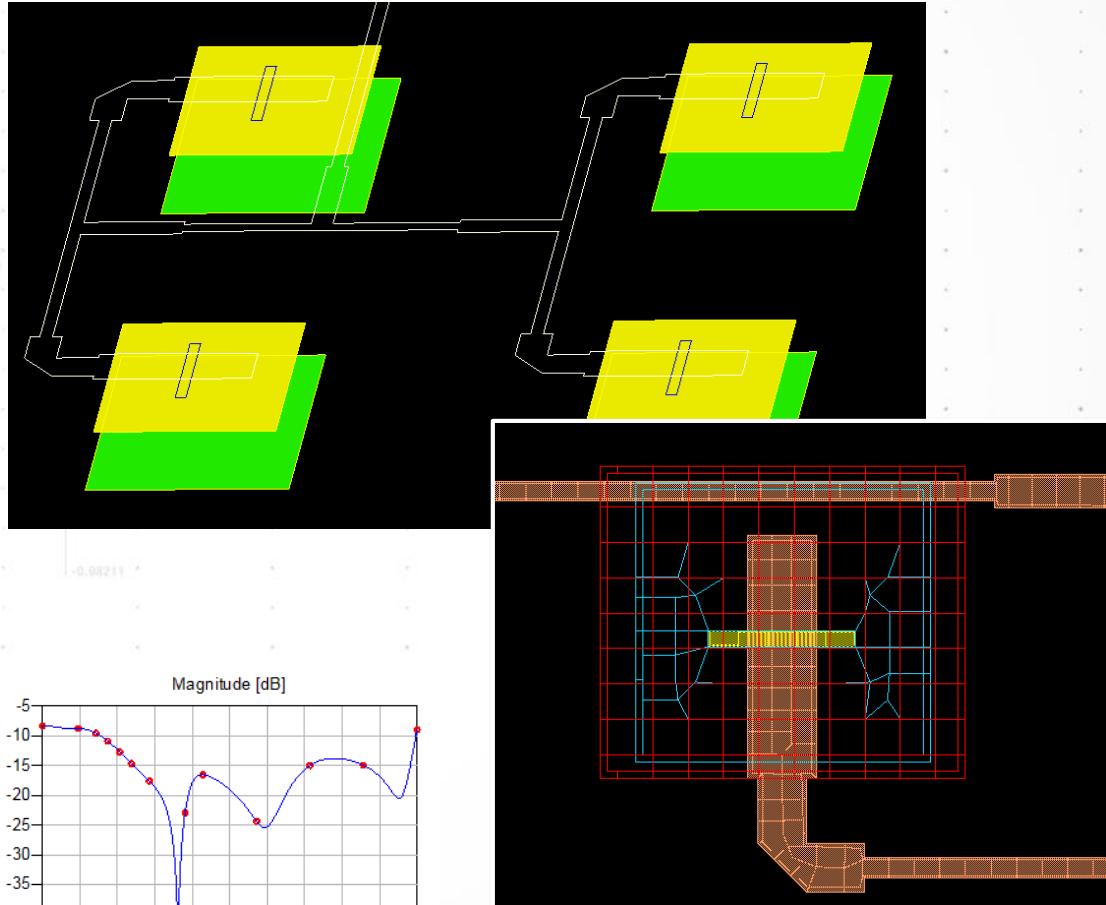
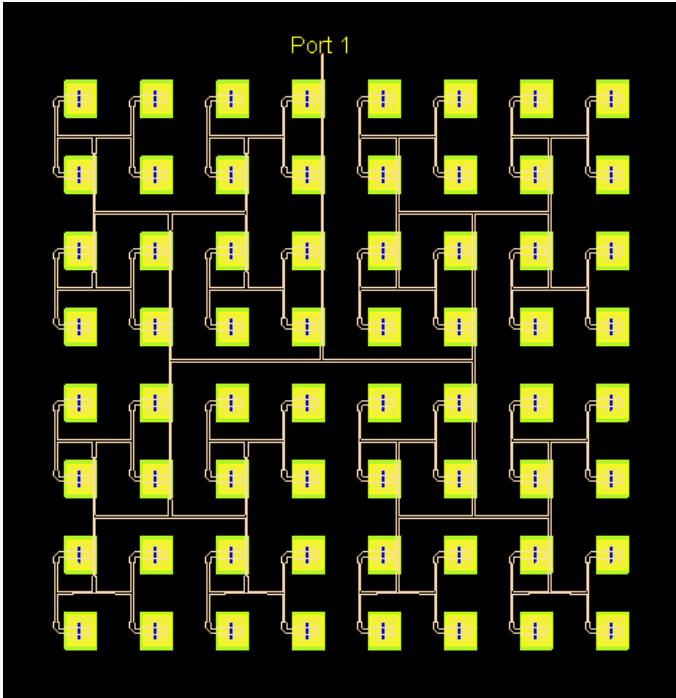
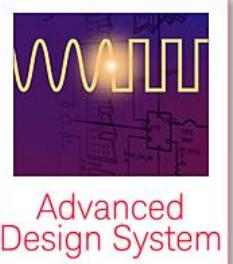


16\*16

4 K80 GPU  
27min, 46s

# 8. Planar Antenna: ADS Momentum 8x8 patch array

Direct EM solution, with weighted signal excitation in post-processor



# Agenda

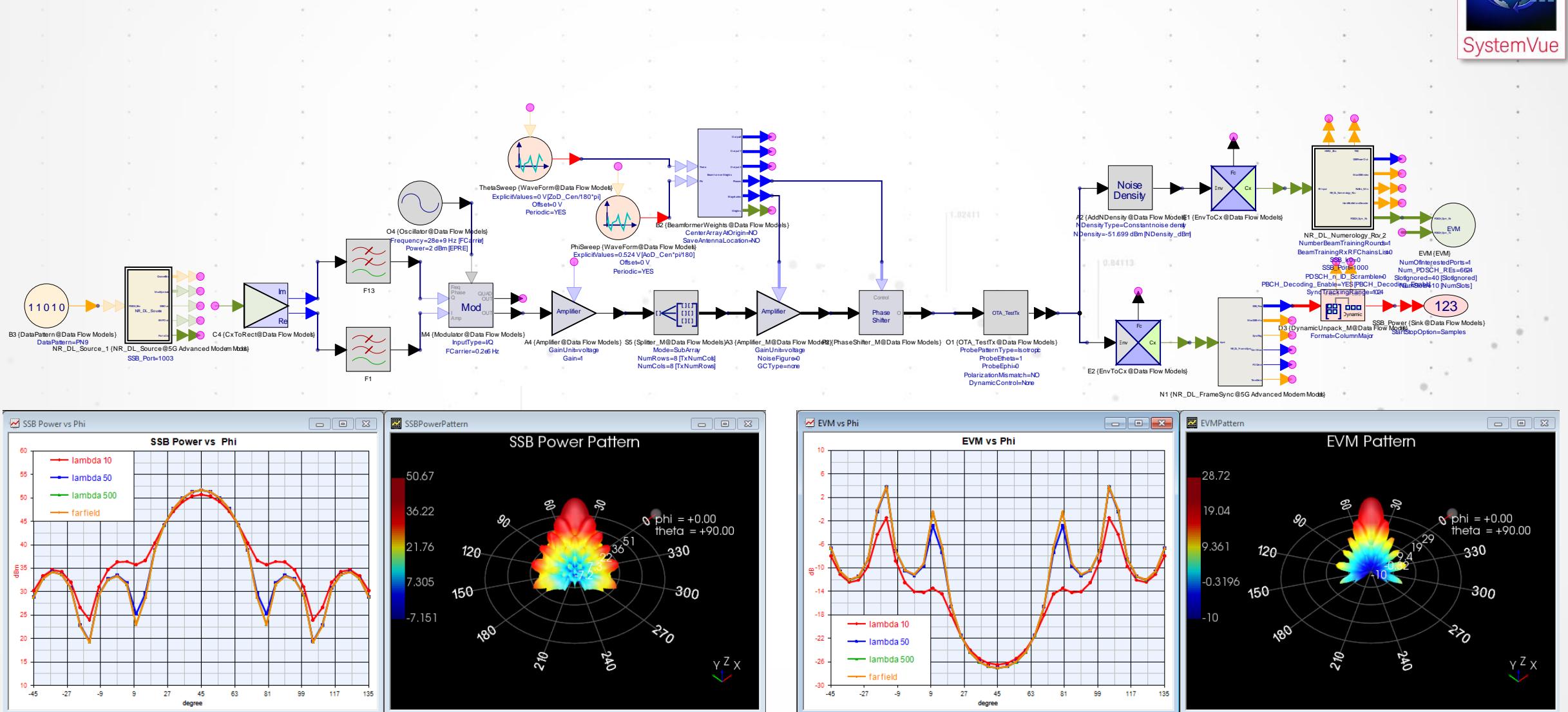
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# 5G NR Base Station Conformance Test Summary-FR 1 & 2

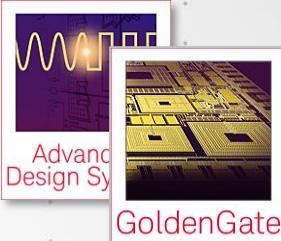
Conducted & radiated conformance tests

Transmitter Characteristics (chp 6)	Receiver Characteristics (chp 7)	Performance Requirements (chp 8)
<ul style="list-style-type: none"><li><b>Transmit Power</b> (TRP, EIRP)</li><li><b>Output Power Dynamics</b> (RE Power Control DR / Total Power DR / ...)</li><li><b>Transmit On/Off Power</b> (TX Off Power / TX Transient Period)</li><li><b>Signal Quality</b> (Freq Error / EVM / Time Alignment Error /...)</li><li><b>Unwanted Emissions</b> (Occupied BW / ALCR / Spurious /...)</li><li><b>Intermodulation</b> (Interference...)</li></ul>	<ul style="list-style-type: none"><li><b>Reference Sensitivity Level</b></li><li><b>Dynamic Range</b></li><li><b>In-Band Selectivity &amp; Blocking Characteristics</b> (Adjacent Channel Selectivity (ACS))</li><li><b>Out-of-Band Blocking</b></li><li><b>Spurious Emissions</b></li><li><b>Intermodulation</b></li><li><b>In-channel Selectivity</b></li></ul>	<ul style="list-style-type: none"><li><b>Performance Requirements for PUSCH</b><ul style="list-style-type: none"><li>Multipath fading propagation for given SNR</li></ul></li><li><b>Performance Requirements for PUCCH</b><ul style="list-style-type: none"><li>ACK missed detection</li><li>NACK to ACK detection</li><li>UCI BLER performance (format 2)</li></ul></li><li><b>Performance Requirements for PRACH</b><ul style="list-style-type: none"><li>False alarm probability and missed detection</li></ul></li></ul>

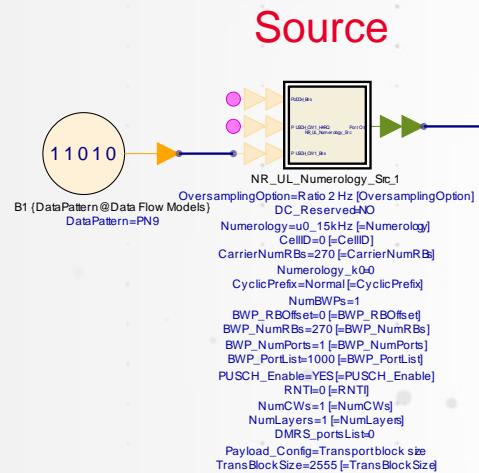
# 9. Verification: 5G NR DL OTA Measurement Simulation



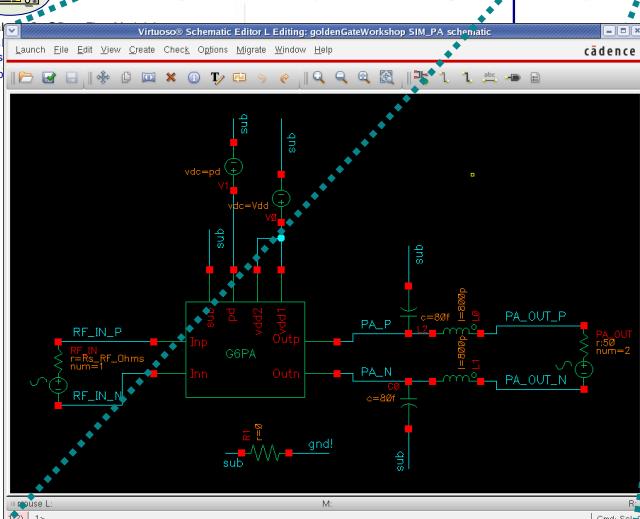
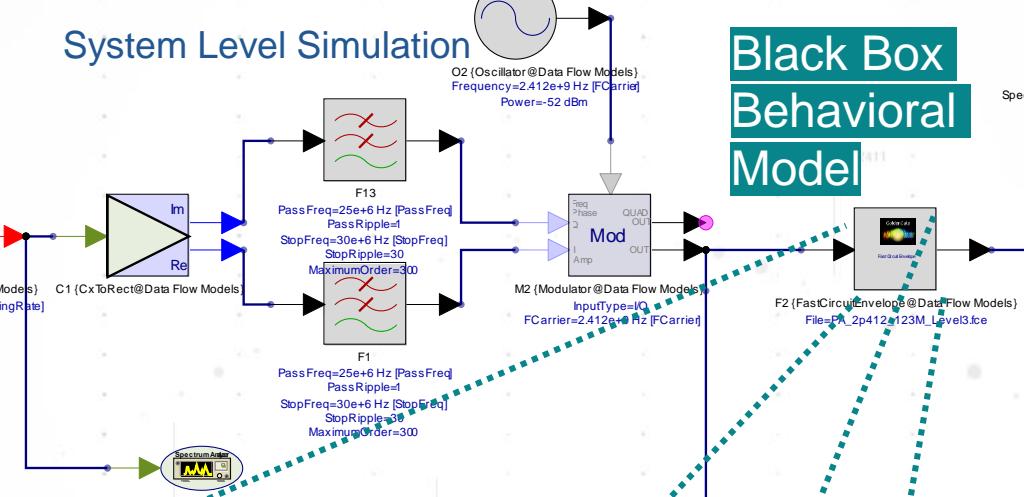
# 9. Verification : Fast-Circuit-Envelop Model



## SystemVue

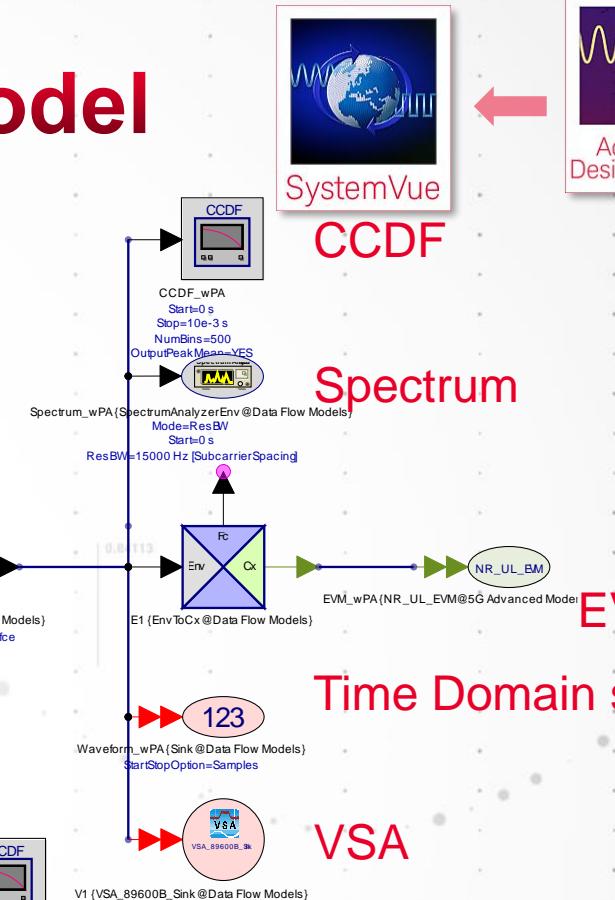


## System Level Simulation



## ADS/ GoldenGate(on Virtuoso)

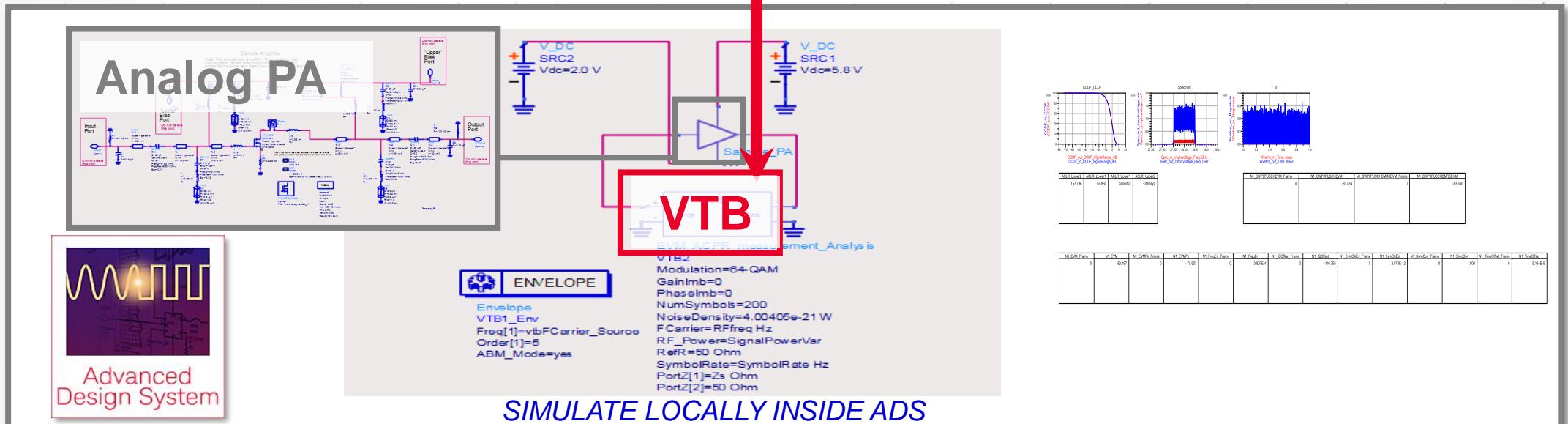
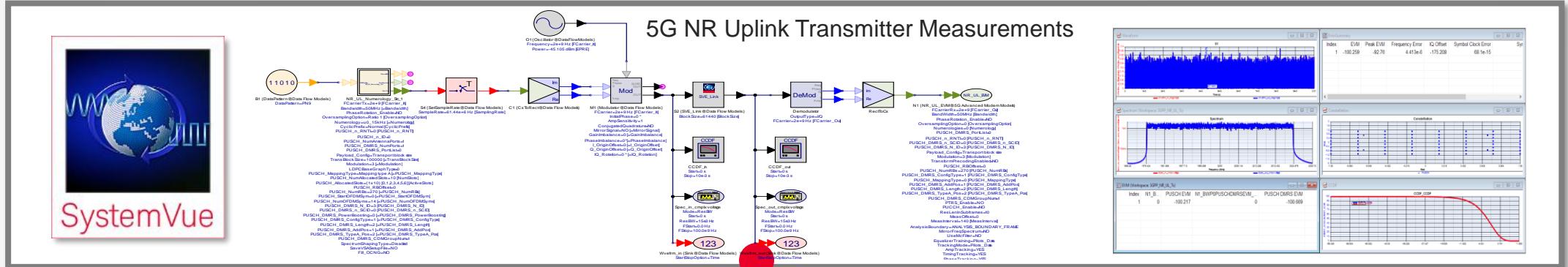
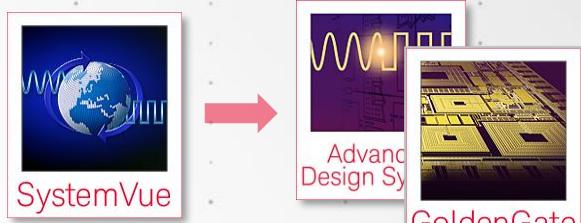
### Circuit Design



Including  
Memory Effect

# 9. Verification : Verification Test Bench

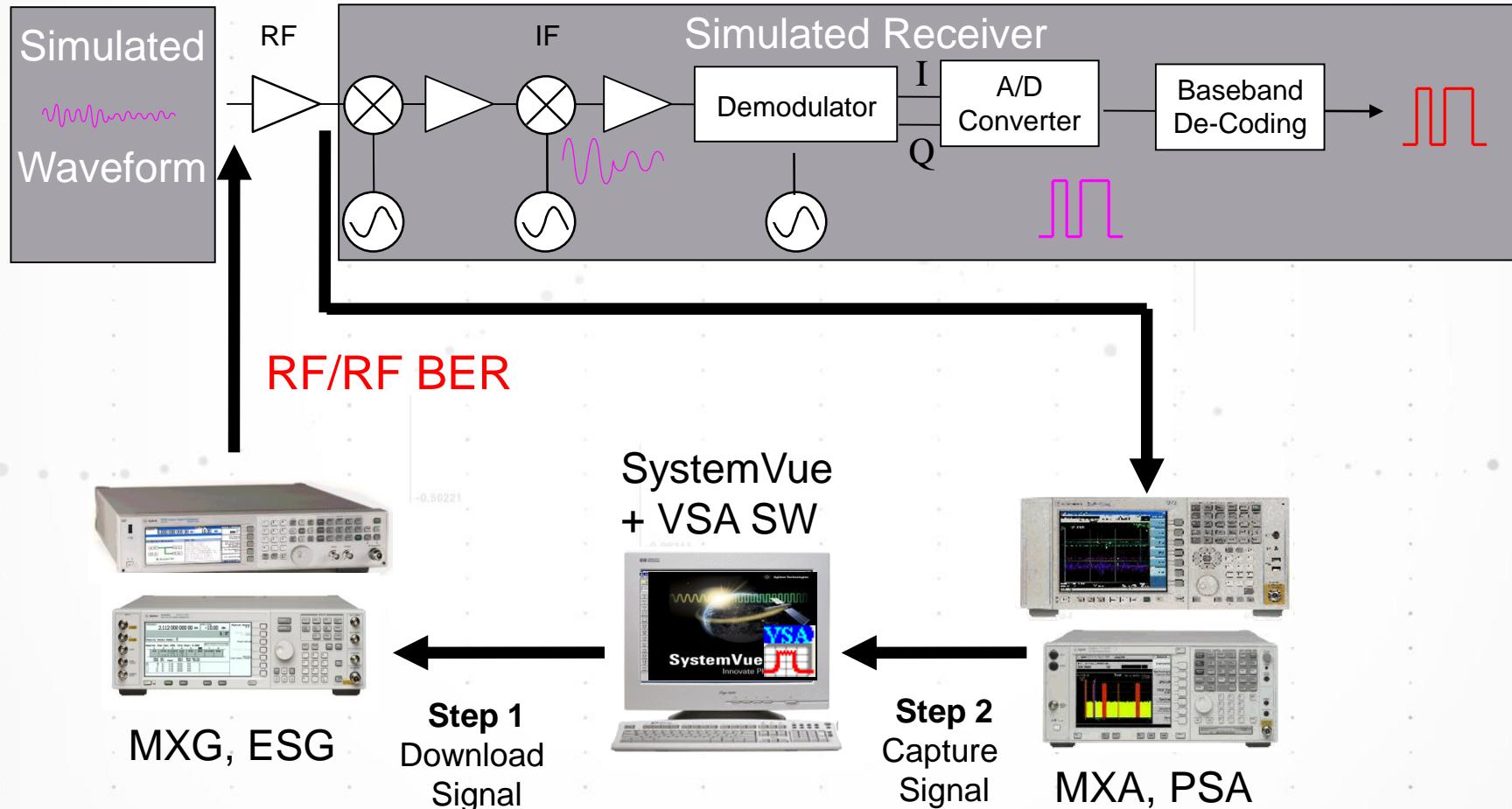
In ADS / GoldenGate



# Agenda

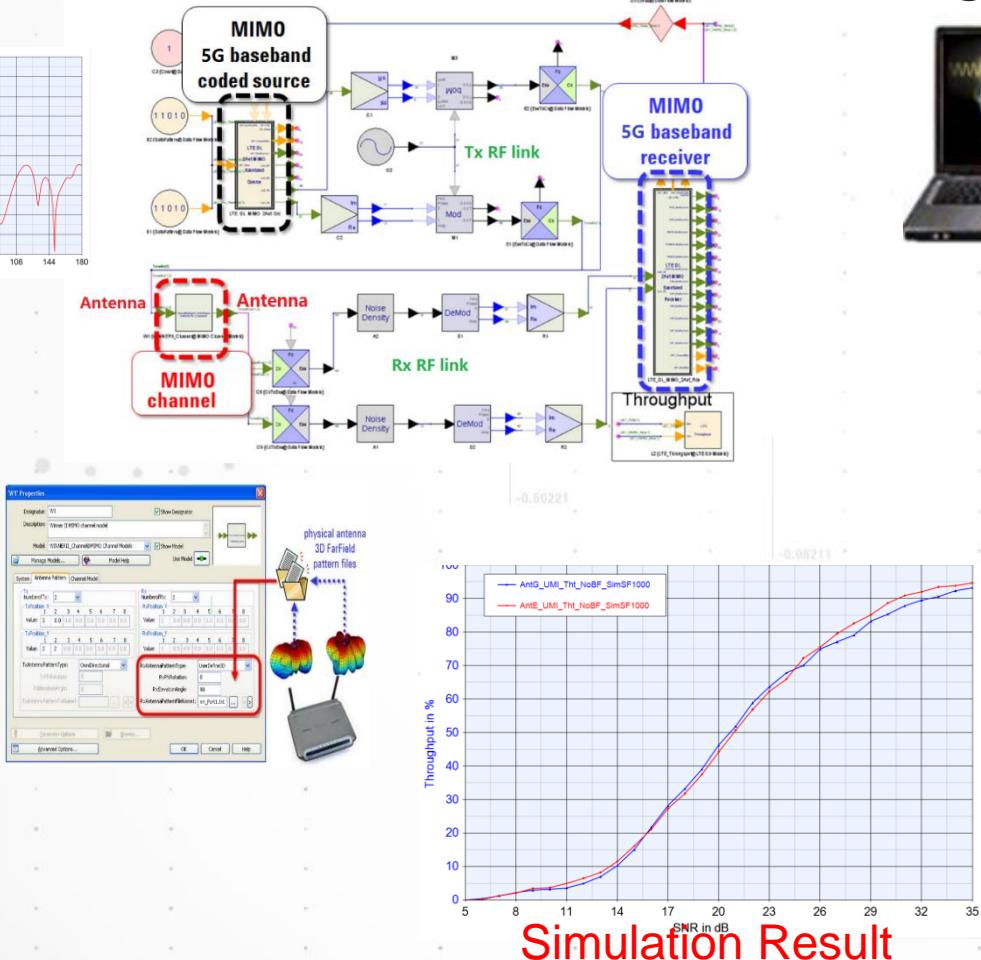
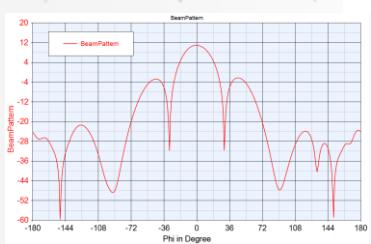
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# Early R&D Hardware Testing - *RF DUT*



# Keysight Phase Array Antenna Test

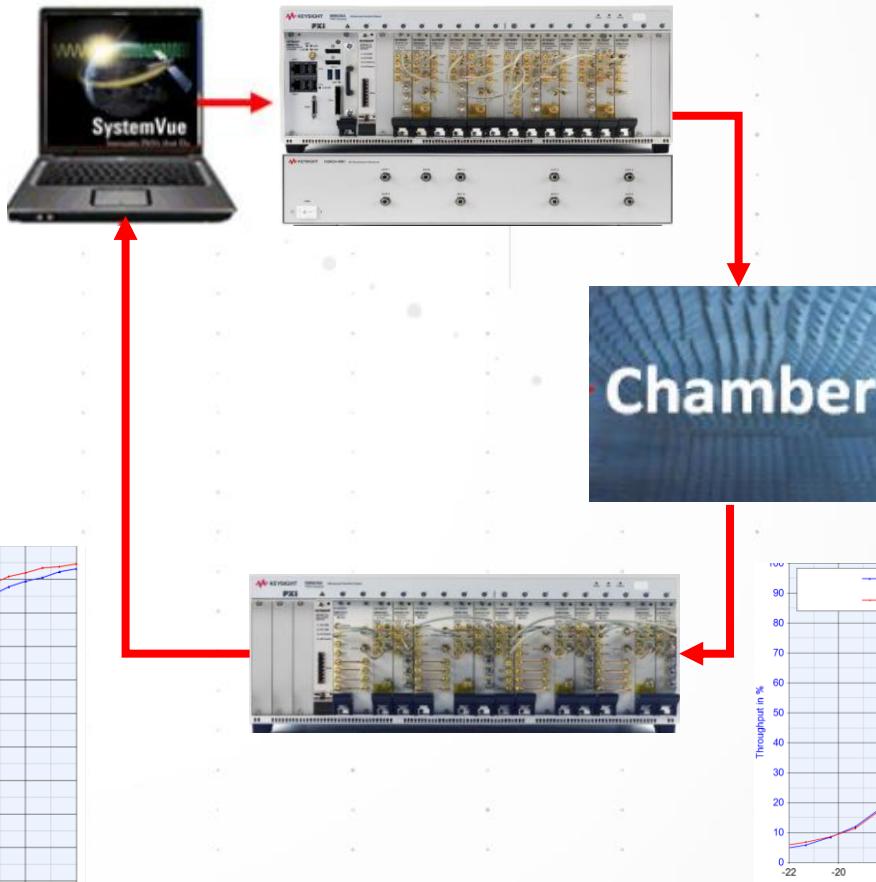
- Throughput/Beam Pattern Simulation in SystemVue to get Antenna Performance baseline



Simulation Result

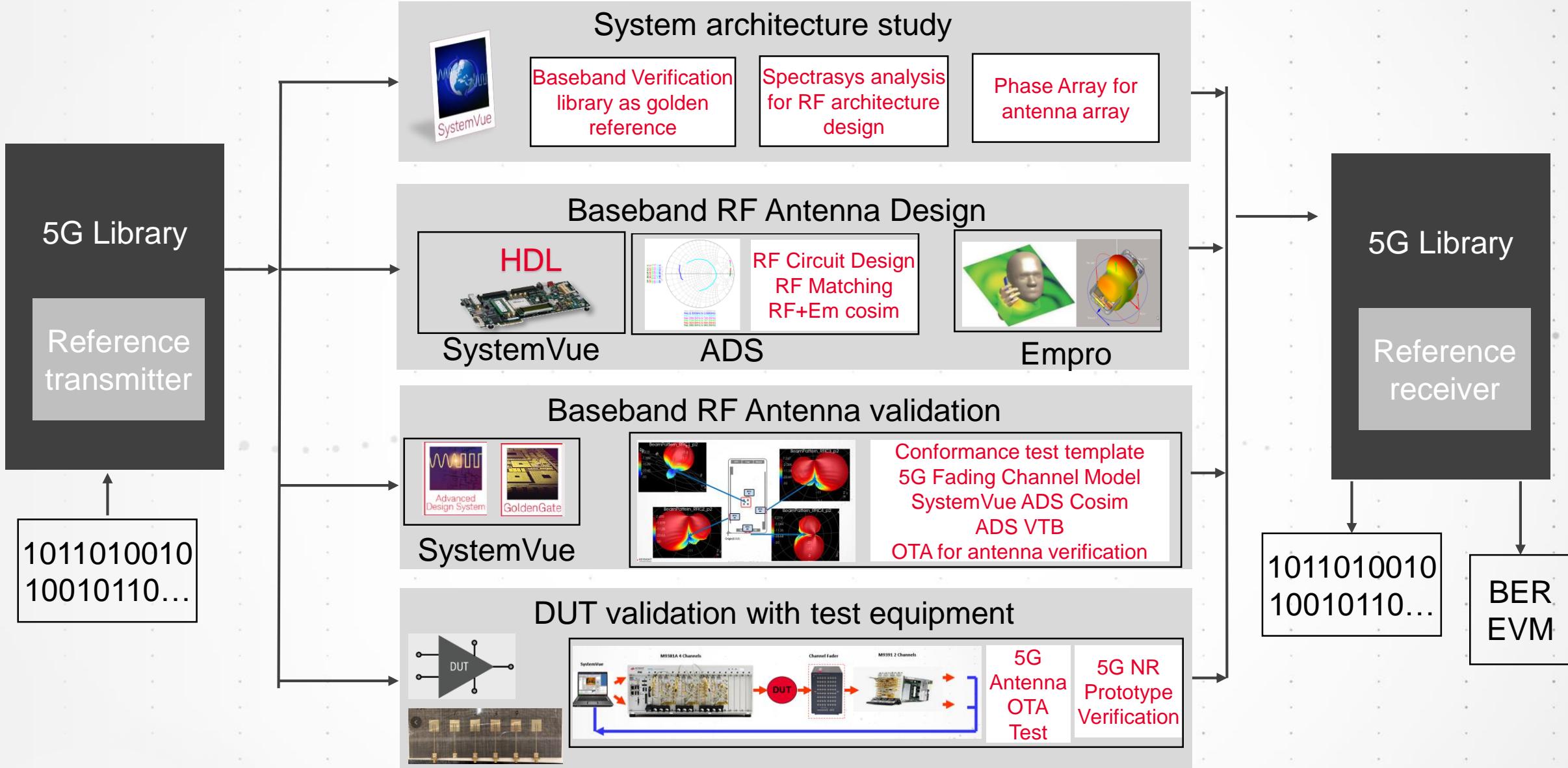
3GPP 5G NR System Design & Verification Solution

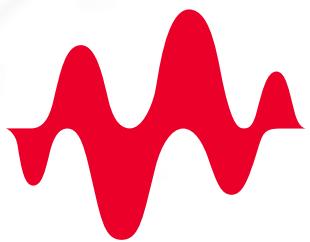
- Massive MIMO Throughput/Beam Pattern OTA Test with Keysight Instruments & SystemVue



Test Result

# Accelerate 5G Design with ADS SystemVue Empro





**KEYSIGHT  
TECHNOLOGIES**

Thank you